



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Brian K. Aegerter  
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Appl. No.: 10/647,914

Conf. No.: 2441

Filed: August 26, 2003

Title: SELECTIVE TREATMENT OF MICROELECTRONIC  
WORKPIECE SURFACES

Art Unit: 1746

Examiner: Kornakov, Michail

Docket No.: 114183-20 (P00-0024US3)

RESPONSE TO OFFICE ACTION DATED JUNE 29, 2005

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir/Madam:

In response to the Office Action dated June 29, 2005, please amend  
the application as follows:

CURRENT LISTING OF CLAIMS begins on page 2 of this paper.

REMARKS/ARGUMENTS begin on page 8 of this paper.

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04 FC:1202

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